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<u>L22</u>	14 and 110	1	<u>L22</u>
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<u>L20</u>	18 and 112	8	<u>L20</u>
<u>L19</u>	18 and 111	3	<u>L19</u>
<u>L18</u>	18 and 110	1	<u>L18</u>
<u>L17</u>	18 and 19	3	<u>L17</u>
<u>L16</u>	16 and 112	8	<u>L16</u>
<u>L15</u>	16 and 111	3	<u>L15</u>
<u>L14</u>	16 and 110	1	<u>L14</u>

<u>L13</u>	l6 and l9	3	<u>L13</u>
<u>L12</u>	(710/305-317)![CCLS]	3977	<u>L12</u>
<u>L11</u>	(716/1-21)[CCLS]	8081	<u>L11</u>
<u>L10</u>	(712/35-38,247)![CCLS]	766	<u>L10</u>
<u>L9</u>	(712/2-300)![CCLS]	10904	<u>L9</u>
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<u>L8</u>	L6 and (cell\$1 or array\$1 or matrices or matirx)	141	<u>L8</u>
<u>L7</u>	L6 and microcontrol\$4	22	<u>L7</u>
<u>L6</u>	L5 and (field near1 programmable or fpga or pld or pla or mcm\$1)	157	<u>L6</u>
<u>L5</u>	(virtual\$4 or standa\$8) near15 (conversion\$1 or convert\$4 or translat\$6) near15 protocol\$1	2713	<u>L5</u>
<u>L4</u>	L3 and (cell\$1 or array\$1 or matrices or matirx)	367	<u>L4</u>
<u>L3</u>	L2 and microcontrol\$4	399	<u>L3</u>
<u>L2</u>	L1 and (field near1 programmable or fpga or pld or pla or mcm\$1)	3019	<u>L2</u>
<u>L1</u>	(virtual\$4 or standa\$8) near15 (conversion\$1 or convert\$4 or translat\$6)	86688	<u>L1</u>

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IEEE CNF IEEE Conference Proceeding

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